	MGT-MODE-3-003		
	V1.02		
			2024-12-12

1

1.1

1.2

	Plc( )
Mc5000,Mc5100	
MEGreator	MC5000 MU
	MEGreator

2

2.1

Mc5x00 Plc

1>. C C ,

2>. Plc , X, Y, M, S, SM, T, C; D, R, SD ,Z,F

X , D

3>. , 64K

4>. ANSI C , C

5>. C ;

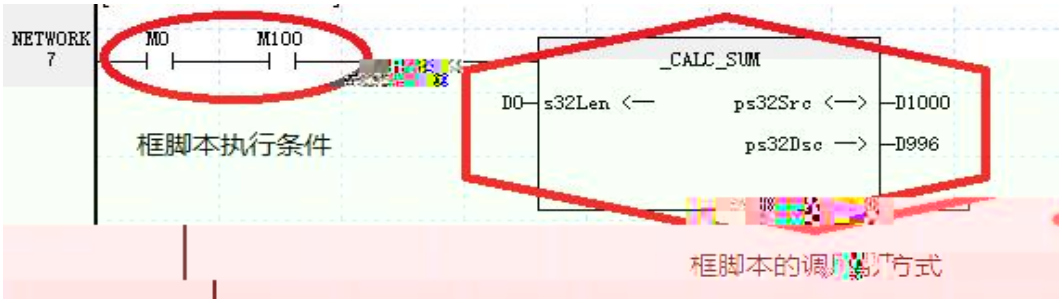
6>.

2.2

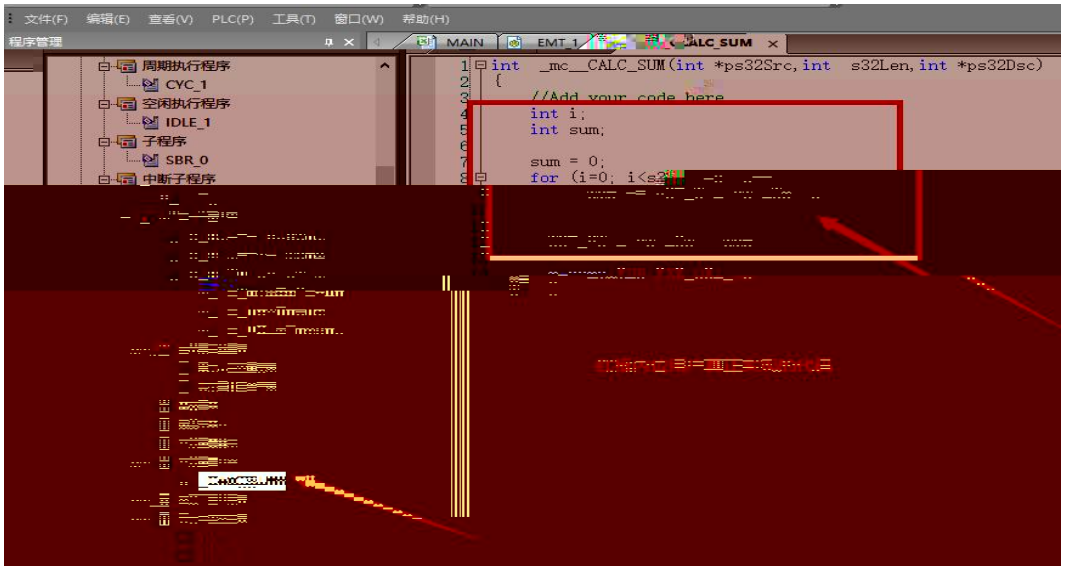
1>

LD LD

LD

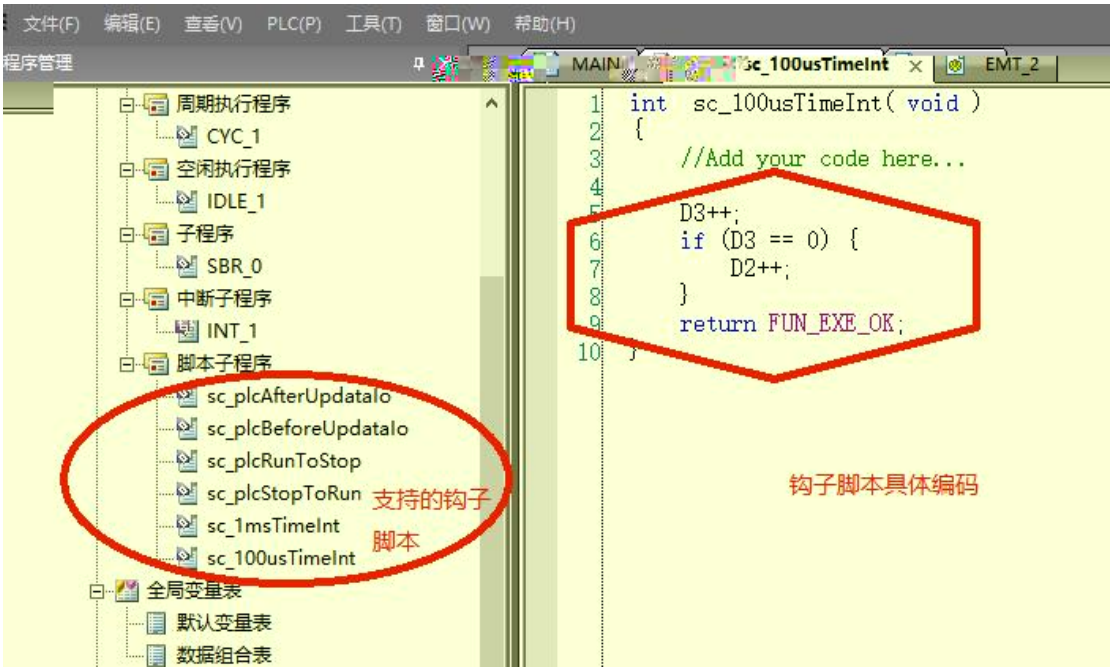


1



2

1>



3

2.3

C

plc

export_module.h	Plc
user_common.c	
user_common.h	

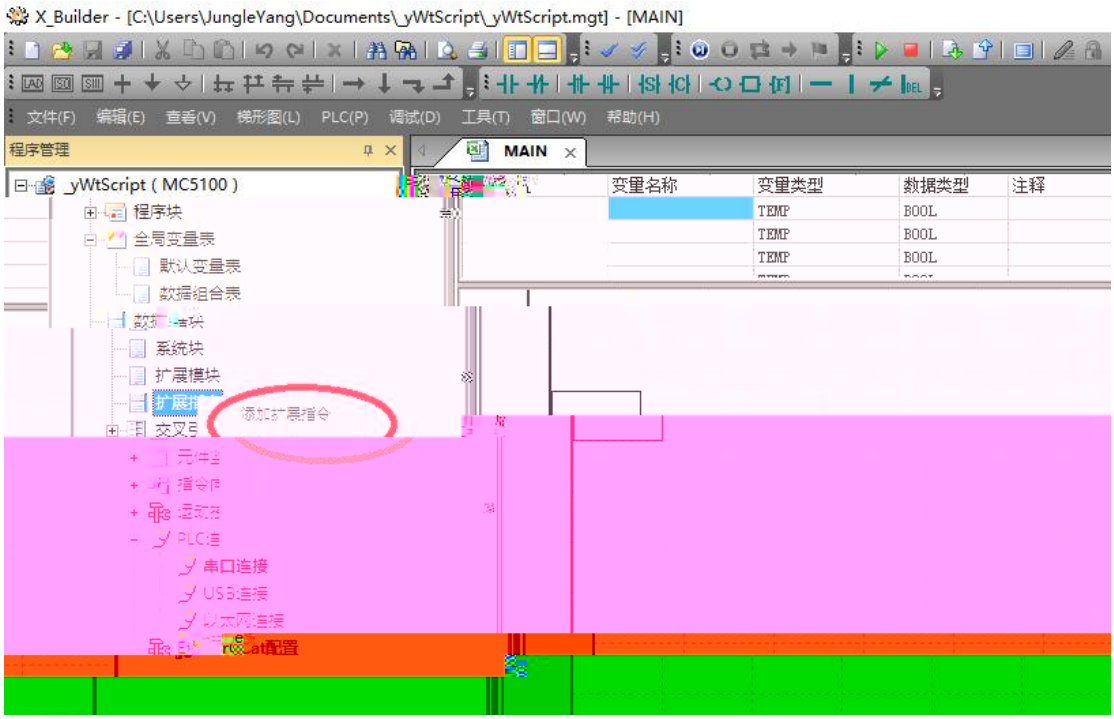
3

xBuild C

D1000 20000 D998( )
D996( )

3.1

“ ” , “ ” “ ”



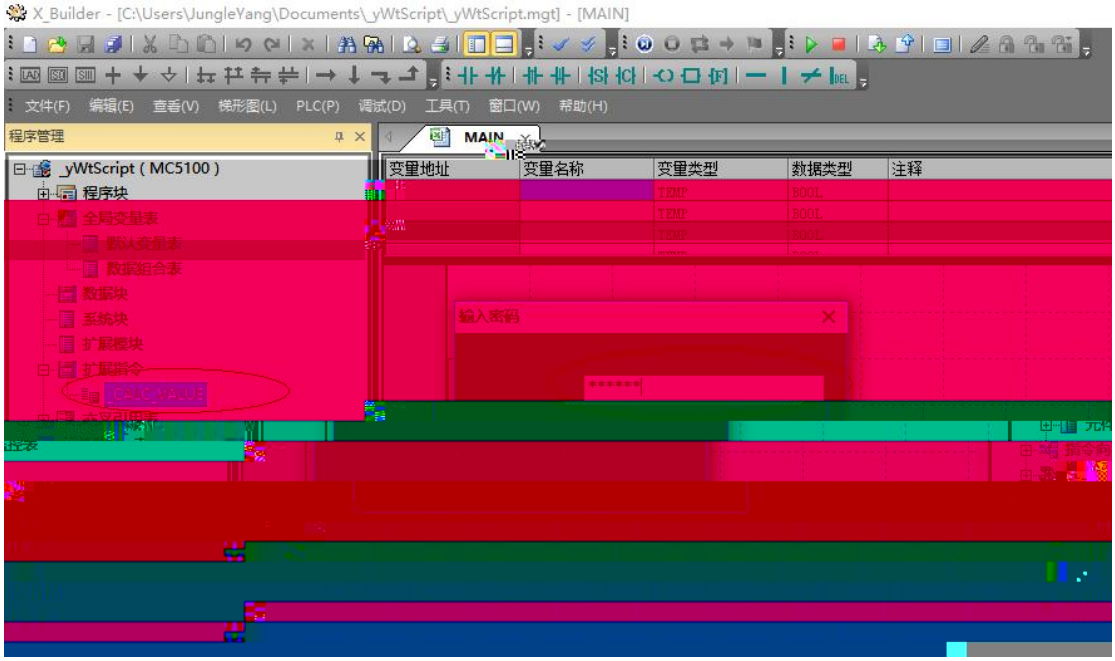
4

3

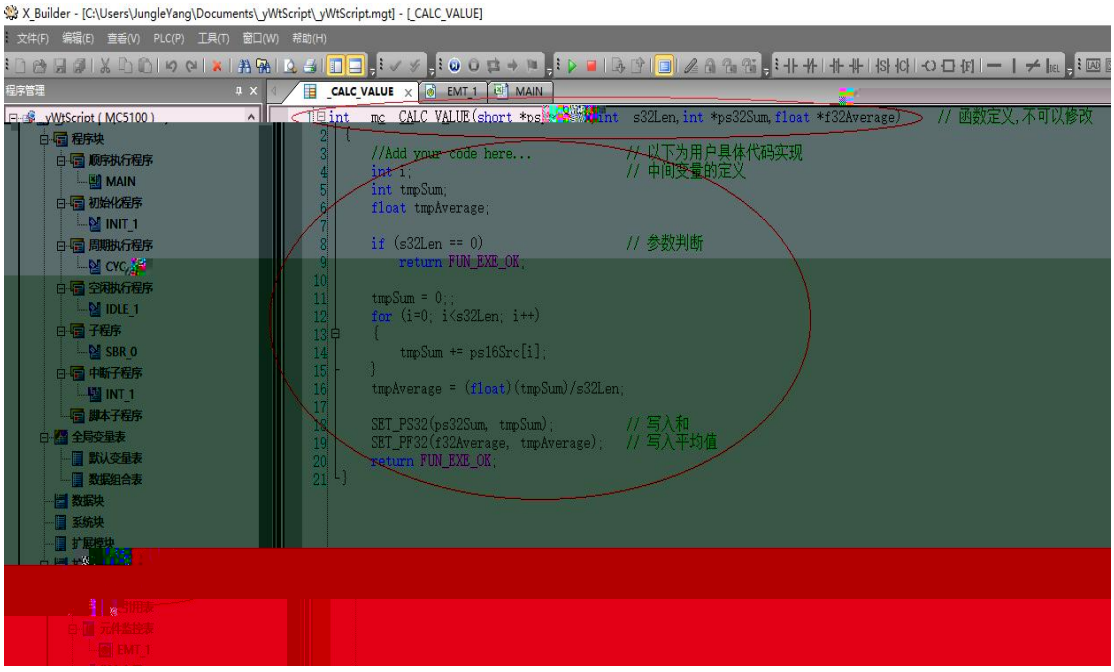
3.6 н н 6

3.3

”_CALC_VALUE”



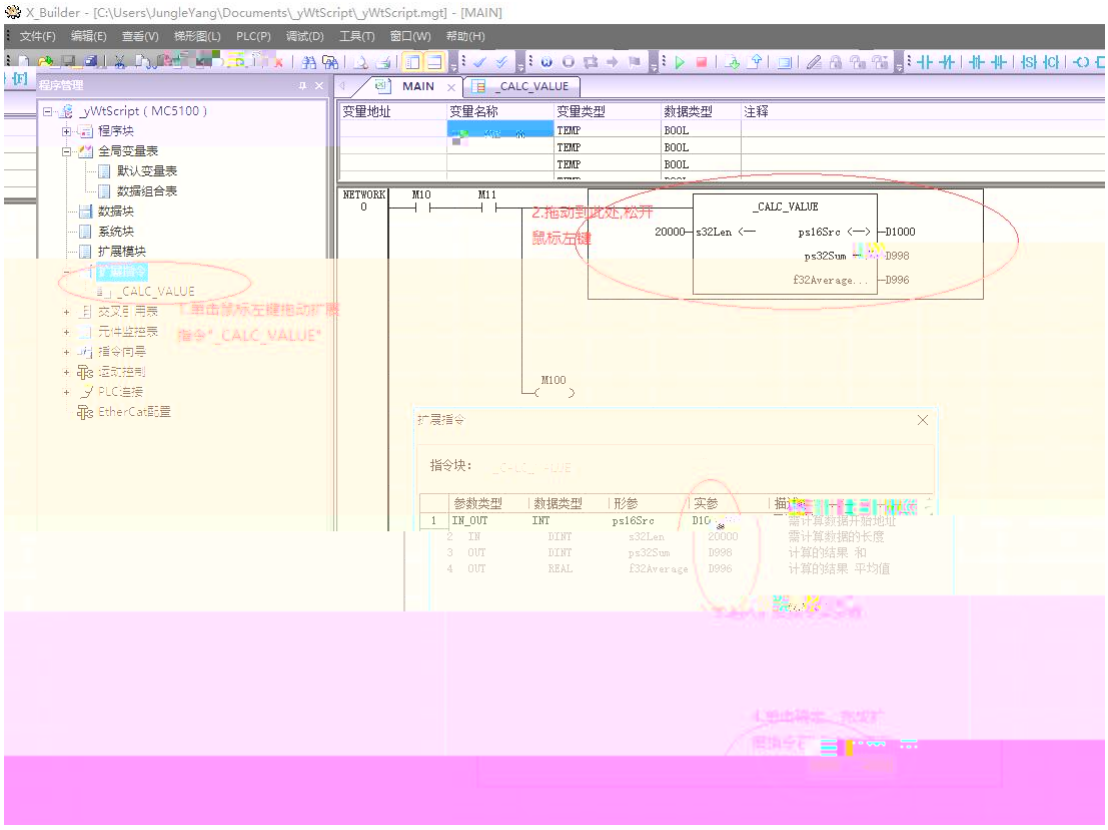
6



7

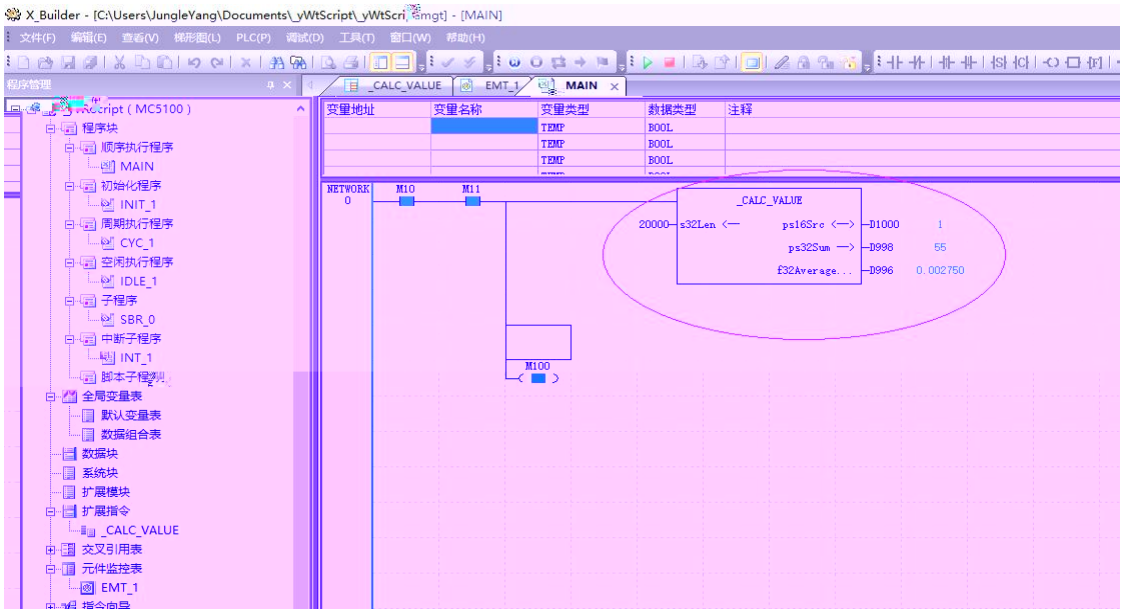
”C ”

3.4



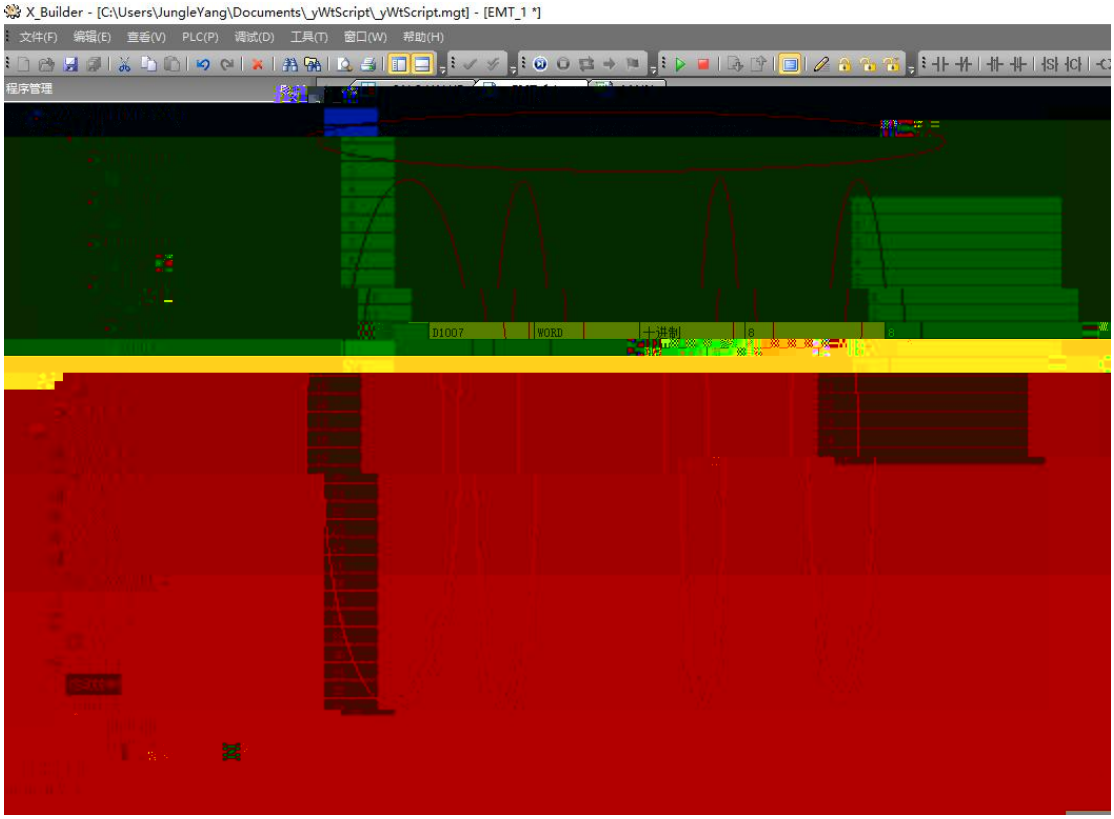
8

M10 M11 On ”_CALC_VALUE” _CALC_VALUE D1000 20000 D998( ) D996( )



9

3.5



10

4

4.1

4.1.1 X

	X
	PLC
	X0 ~ X7777
	Bit
	8 , ’X’

X[10] **X[XX]**

4.1.2 Y

	Y
	PLC
	Y0 ~ Y7777
	Bit
	8 , ’Y’

Y[15] **Y[XX]**

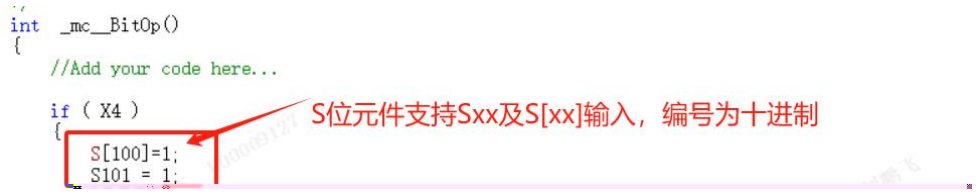
4.1.3 SM

	SM
	PLC SM
	SM0 ~ SM4095
	Bit
	10 , ’SM’

SM

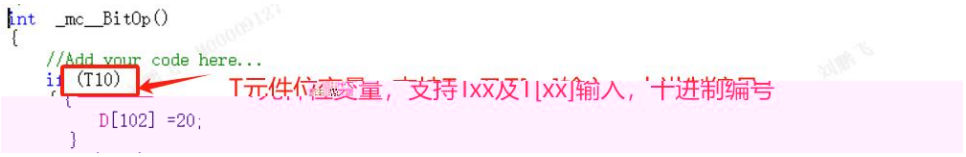
4.1.4 S

	S
	PLC S
	S0 ~ S4095
	Bit
	10 , 'S'



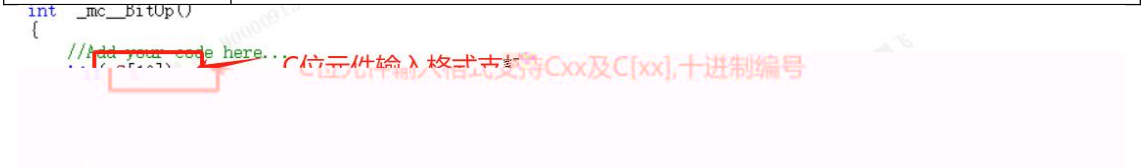
4.1.5 T

	T
	PLC T
	T0 ~ T4095
	Bit
	10 , 'T'



4.1.6 C

	C
	PLC C
	C0 ~ C4095
	Bit
	10 , 'C'



4.1.7 M

	C
	PLC M
	M0 ~ M65535
	Bit
	10 , ’M’

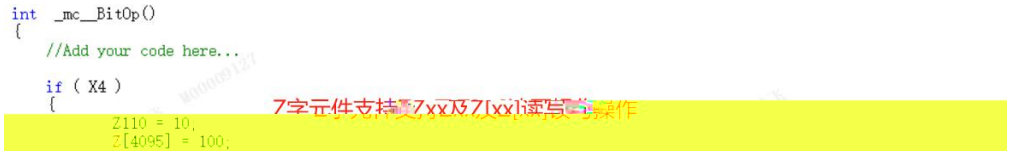
4.1.8 SD

	SD
	PLC SD
	SD0 ~ SD4095
	signed short
	10 , ’SD’

32 SD 16 SD 32

4.1.9 Z

	Z
	PLC Z
	Z0 ~ Z4095
	signed short
	10 , ’Z’



4.1.10 D

	D
	PLC D
	D0 ~ D65535
	signed short
	10 , ’D’

4.1.11 R

	R
	PLC R
	R0 ~ R65535
	signed short
	10 , ’R’

4.2

MC5000 PLC 32

4.2.1 D

	int GET_DD(unsigned short stNum)
	“ D ”
	stNum: D
	int,

4.2.2 D

	void SET_DD(unsigned short stNum, int val)
	“D”
	stNum: D
	val :

4.2.3 D

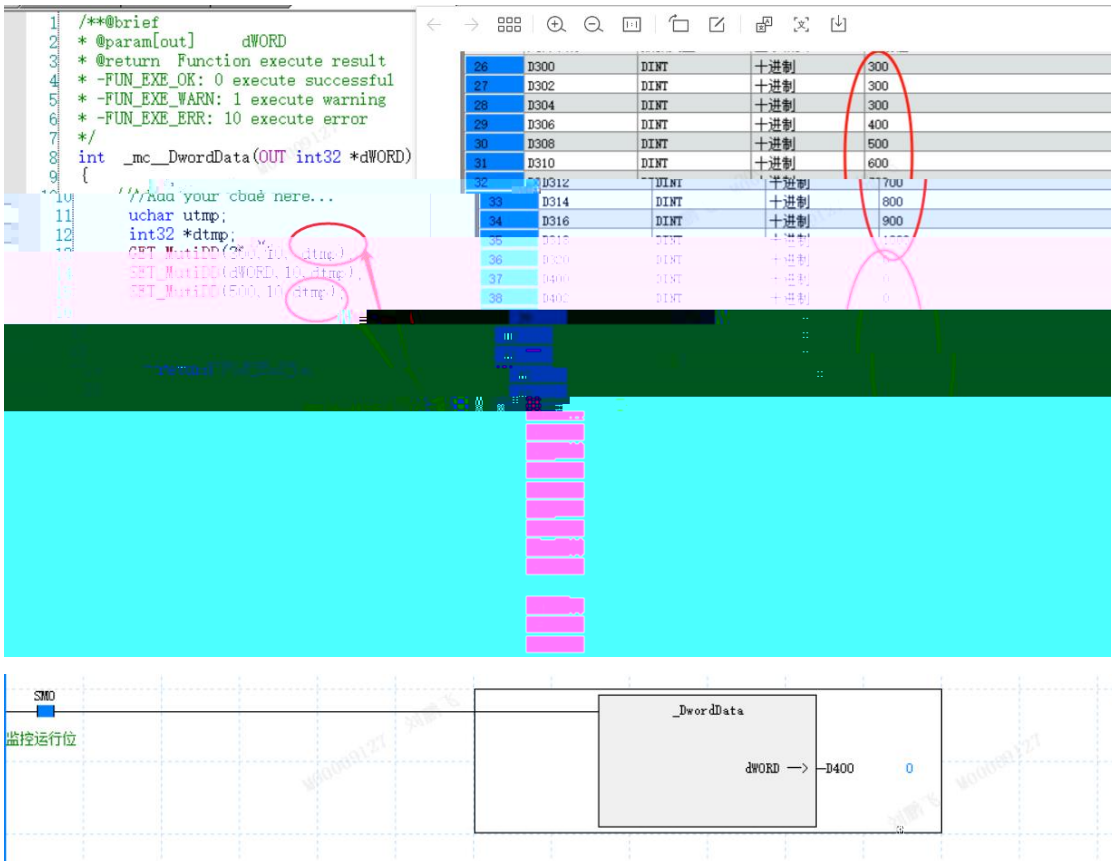
	int GET_MutiDD(int stNum, int len, int *ps32Dsc)
	“D”
	stNum: D
	Len :
	ps32Dsc:
	0 ,

4.2.4

4.2.4 D

	int SET_MutiDD(int stNum, int len, int *ps32Src)
	“D”
	stNum: D
	Len :

	ps32Dsc: D
	0 ,



1 GET_MutiDD(int stNum, int len, int *ps32Dsc)
SET_MutiDD(int stNum, int len, int *ps32Src) D
dWORD D400 D400 10

2 int *ps32Dsc int *ps32Src

4.2.5 D

	float GET_FD(unsigned short stNum)
	“ D ”
	stNum: D
	float,

4.2.6

4.2.6 D

	void SET_FD(unsigned short stNum, float val)
	“D”
	stNum: D
	val :

```
int  _mc__DwordData(OUT int32 *dWORD)
{
    //Add your code here...
    uchar utmp;
    int32 *dtmp;
    float fTmp , *pfTmp;
    fTmp = GET_FD(600);
    SET_FD(700,fTmp);
```

读取d600内浮点数存入变量fTmp中

将浮点数fTmp的值写入D700中

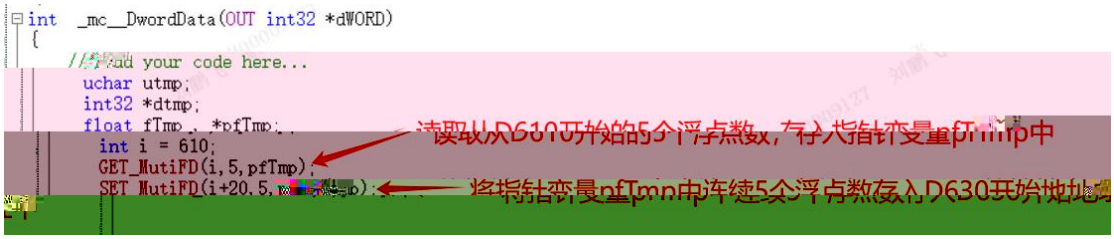
4.2.7 D

	int GET_MutiFD(int stNum, int len, float *pf32Dsc)
	“D”
	stNum: D
	Len :
	ps32Dsc:
	0 ,

4.2.8

4.2.8 D

	int SET_MutiFD(int stNum, int len, float *pf32Src)
	“D”
	stNum: D
	Len :
	ps32Dsc: D
	0 ,



i 16

4.2.9 R

R D

D

	int GET_DR(unsigned short stNum)
	“R”
	stNum: R
	int,

4.2.10 R

	void SET_DR(unsigned short stNum, int val)
	“R”
	stNum: R
	val :

4.2.11 R

	int GET_MutiDR(int stNum, int len, int *ps32Dsc)
	“R”
	stNum: R
	Len :
	ps32Dsc:
	0 ,

4.2.12 R

	int SET_MutiDR(int stNum, int len, int *ps32Src)
	“R”
	stNum: R
	Len :
	ps32Dsc: R
	0 ,

4.2.13 R

	float GET_FR(unsigned short stNum)
	“R”
	stNum: R
	float,

4.2.14 R

	void SET_FR(unsigned short stNum, float val)
	“R”
	stNum: R
	val :

4.2.15 R

	int GET_MutiFR(int stNum, int len, float *pf32Dsc)
	“R”
	stNum: R
	Len :
	ps32Dsc:

	0 ,

4.2.16 R

	int SET_MutiFR(int stNum, int len, float *pf32Src)
	“R”
	stNum: R
	Len :
	ps32Dsc: R
	0 ,

4.2.17 F F0 ~ F9

	int GET_DF(int stNum)
	“F”
	stNum: F
	int,

4.2.34 F0~F9

4.2.18 F F0 ~ F9

	void SET_DF(int stNum, int val)
	“F”
	stNum: F
	val :

4.2.34 F0~F9

4.2.19 F F0 ~ F9

	int GET_MutiDF(int stNum, int len, int *ps32Dsc)
	“F”

	stNum: F
	Len :
	ps32Dsc:
	0 ,

4.2.34 F0~F9

4.2.20 F F0 ~ F9

	int SET_MutiDF(int stNum, int len, int *ps32Src)
	“ F ”
	stNum: F
	Len :
	ps32Dsc: F
	0 ,

4.2.34 F0~F9

4.2.21 F F0 ~ F9

	float GET_FF(unsigned short stNum)
	“ F ”
	stNum: F
	float,

4.2.34 F0~F9

4.2.22 F F0 ~ F9

	void SET_FF(unsigned short stNum, float val)
	“ F ”
	stNum: F
	val :

4.2.34 F0~F9

4.2.23 F F0 ~ F9

	int GET_MutiFF(int stNum, int len, float *pf32Dsc)
	“F”
	stNum: F
	Len :
	ps32Dsc:
	0 ,

4.2.34 F0~F9

4.2.24 F F0 ~ F9

	int SET_MutiFF(int stNum, int len, float *pf32Src)
	“F”
	stNum: F
	Len :
	ps32Dsc: F
	0 ,

4.2.34 F0~F9

4.2.25 Fx F0 ... F9

	int GET_DF0(int stNum)
	int GET_DF1(int stNum)
	int GET_DF2(int stNum)
	int GET_DF3(int stNum)
	int GET_DF4(int stNum)
	int GET_DF5(int stNum)
	int GET_DF6(int stNum)
	int GET_DF7(int stNum)
	int GET_DF8(int stNum)
	int GET_DF9(int stNum)
	“Fx”
	stNum: Fx
	int,

4.2.34 F0~F9

4.2.26 Fx F0 ... F9

	void SET_DF0(int stNum, int val)
	void SET_DF1(int stNum, int val)
	void SET_DF2(int stNum, int val)
	void SET_DF3(int stNum, int val)
	void SET_DF4(int stNum, int val)
	void SET_DF5(int stNum, int val)
	void SET_DF6(int stNum, int val)
	void SET_DF7(int stNum, int val)
	void SET_DF8(int stNum, int val)
	void SET_DF9(int stNum, int val)
	“ Fx ”
	stNum: Fx
	val :

4.2.34 F0~F9

4.2.27 Fx F0 ... F9

	int GET_MutiDF0(int stNum, int len, int *ps32Dsc)
	int GET_MutiDF1(int stNum, int len, int *ps32Dsc)
	int GET_MutiDF2(int stNum, int len, int *ps32Dsc)
	int GET_MutiDF3(int stNum, int len, int *ps32Dsc)
	int GET_MutiDF4(int stNum, int len, int *ps32Dsc)
	int GET_MutiDF5(int stNum, int len, int *ps32Dsc)
	int GET_MutiDF6(int stNum, int len, int *ps32Dsc)
	int GET_MutiDF7(int stNum, int len, int *ps32Dsc)
	int GET_MutiDF8(int stNum, int len, int *ps32Dsc)
	int GET_MutiDF9(int stNum, int len, int *ps32Dsc)
	“ Fx ”
	stNum: Fx
	Len :
	ps32Dsc:
	0 ,

4.2.34 F0~F9

4.2.28 Fx F0 ... F9

	int SET_MutiDF0(int stNum, int len, int *ps32Src)
	int SET_MutiDF1(int stNum, int len, int *ps32Src)
	int SET_MutiDF2(int stNum, int len, int *ps32Src)
	int SET_MutiDF3(int stNum, int len, int *ps32Src)
	int SET_MutiDF4(int stNum, int len, int *ps32Src)
	int SET_MutiDF5(int stNum, int len, int *ps32Src)
	int SET_MutiDF6(int stNum, int len, int *ps32Src)
	int SET_MutiDF7(int stNum, int len, int *ps32Src)
	int SET_MutiDF8(int stNum, int len, int *ps32Src)
	int SET_MutiDF9(int stNum, int len, int *ps32Src)
	“ F ”
	stNum: F
	Len :
	ps32Dsc: F
	0 ,

4.2.34 F0~F9

4.2.29 Fx F0 ... F9

	float GET_FF0(unsigned short stNum)
	float GET_FF1(unsigned short stNum)
	float GET_FF2(unsigned short stNum)
	float GET_FF3(unsigned short stNum)
	float GET_FF4(unsigned short stNum)
	float GET_FF5(unsigned short stNum)
	float GET_FF6(unsigned short stNum)
	float GET_FF7(unsigned short stNum)
	float GET_FF8(unsigned short stNum)
	float GET_FF9(unsigned short stNum)
	“ Fx ”
	stNum: Fx
	float,

4.2.34 F0~F9

4.2.30 Fx F0 … F9

	void SET_FF0(unsigned short stNum, float val)
	void SET_FF1(unsigned short stNum, float val)
	void SET_FF2(unsigned short stNum, float val)
	void SET_FF3(unsigned short stNum, float val)
	void SET_FF4(unsigned short stNum, float val)
	void SET_FF5(unsigned short stNum, float val)
	void SET_FF6(unsigned short stNum, float val)
	void SET_FF7(unsigned short stNum, float val)
	void SET_FF8(unsigned short stNum, float val)
	void SET_FF9(unsigned short stNum, float val)
	“ Fx ”
	stNum: Fx
	val :

4.2.34 F0~F9

4.2.31 Fx F0 … F9

```
int GE                                  int len, float *pf32Dsc)
int GET_MutiFF1(int stNum, int len, float *pf32Dsc)
int GET_MutiFF2(int stNum, int len, float *pf32Dsc)
int GET_MutiFF3(int stNum, int len, float *pf32Dsc)
int GET_MutiFF4(int stNum, int len, float *pf32Dsc)
int GET_MutiFF5(int stNum, int len, float *pf32Dsc)
int GET_MutiFF6(int stNum, int len, float *pf32Dsc)
int GET_MutiFF7(int stNum, int len, float *pf32Dsc)
int GET_MutiFF8(int stNum, int len, float *pf32Dsc)
int GET_MutiFF9(int stNum, int len, float *pf32Dsc)
```

“ Fx ”

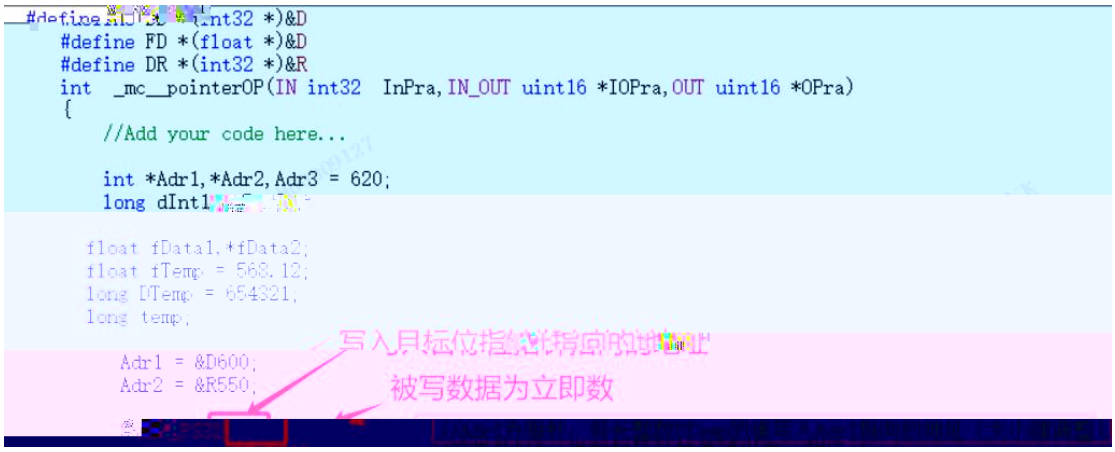
4.2.34 Fx F0 … F9

	void SET_F0(int stNum, signed short val)
	void SET_F1(int stNum, signed short val)
	void SET_F2(int stNum, signed short val)
	void SET_F3(int stNum, signed short val)
	void SET_F4(int stNum, signed short val)
	void SET_F5(int stNum, signed short val)
	void SET_F6(int stNum, signed short val)
	void SET_F7(int stNum, signed short val)
	void SET_F8(int stNum, signed short val)
	void SET_F9(int stNum, signed short val)
	“ Fx ”
	stNum: Fx
	val :

F0~F9

4.2.35 int ( )

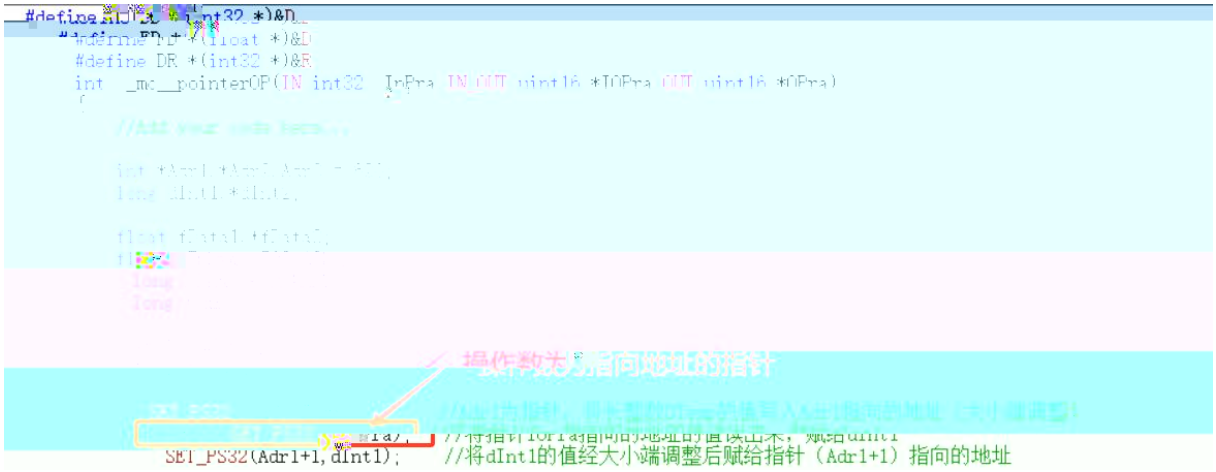
	void SET_PS32(int *ps32Dsc, int s32Src)
	s32Src ps32Dsc
	s32Src:
	ps32Dsc:



Adr1 D600 ADr2 R550

4.2.36 int ( )

	int GET_PS32(int *ps32Src)
	int
	ps32Src:



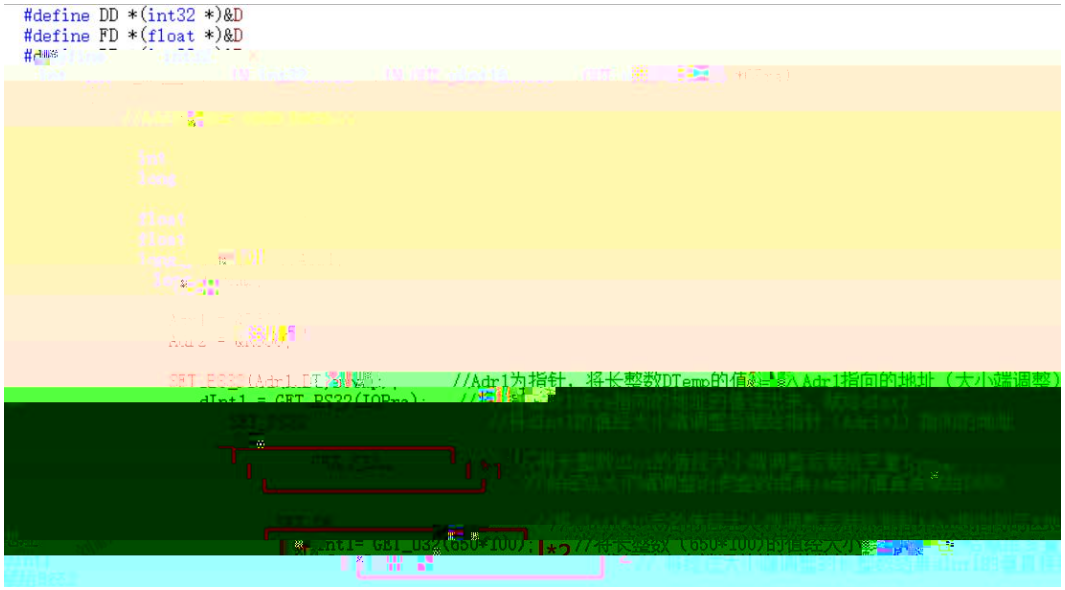
IOPra

4.2.37 int ( )

	int GET_S32(int s32Src)
	int 32
	s32Src:

	PLC

*1



4.2.38 unsigned int ( )

	void SET_PU32(unsigned int *pu32Dsc, unsigned int u32Src)
	u32Src pu32Dsc
	u32Src:
	pu32Dsc:

4.4.23

4.2.39 unsigned int ( )

	unsigned int GET_PU32(unsigned int *pu32Src)
	int
	pu32Src:

4.4.23

4.2.40 unsigned int ( )

	unsigned int GET_U32(unsigned int u32Src)
	Unsigned int 32
	u32Src:
	PLC

*2

4.2.41 float ( )

	void SET_PF32(float *pf32Dsc, float f32Src)
	f32Src pf32Dsc
	f32Src:
	pf32Dsc:

4.4.23

4.2.42 float ( )

	float GET_PF32(float *pf32Src)
	int
	pf32Src:

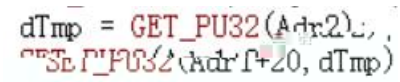
4.4.23

4.2.43 float ( )

	float GET_F32(float f32Src)
	int
	f32Src:

1 +1 PLC 2

2 GET_F32 float Src



PLC

FD[40]=GET_F32(fTemp);

5

5.1.1 modbus crc

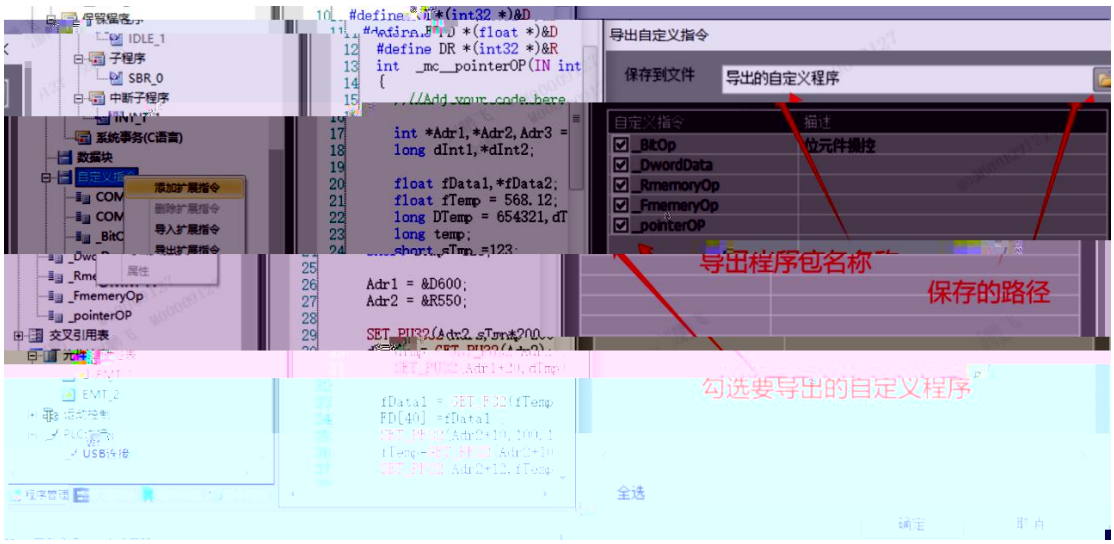
	unsigned short _ycrcModbus(unsigned char *data,unsigned int length)
	Modbus crc
	data: Crc
	Length: Crc
	Crc

5.1.2 ccit crc

	unsigned short _ycrcCcitt(unsigned char *ptr, unsigned int len)
	ccitt
	data: Crc
	Length: Crc
	Crc

6

:


导出自定义指令
保存到文件
导出的自定义程序
自定义指令
描述
_BitOp
位元件操控
_DwordData
_RmemoryOp
_FmemoryOp
_pointerOP
导出程序包名称
保存的路径
勾选要导出的自定义程序
全选